ABSTRACT OF THE DISCLOSURE

System for automatically performing a frequency response equalization tuning utilizes a central processing unit (CPU) in an electronic device to simulate and generate a standard sound signal having a predetermined bandwidth

5 through a sound control circuit in order for a microphone to receive the signal for sending back to the CPU through the sound control circuit performing a frequency response matching with respect to a predetermined ideal frequency response data, calculating a set of equalization tuning gains, and storing the gains in a digital equalizer for automatically performing a frequency response

10 equalization tuning on the speaker.